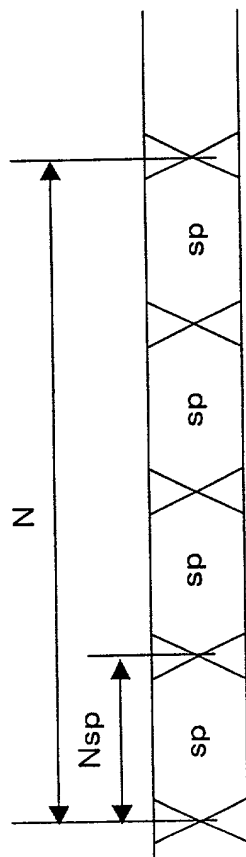


FIG. 1 (PRIOR ART)

FIG. 2A
(PRIOR ART)



GUARD INTERVAL INSERTION

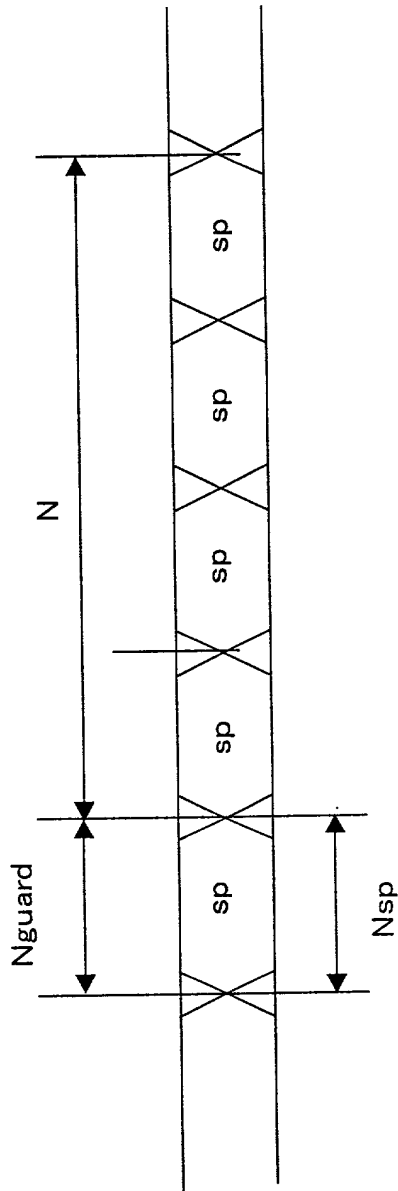


FIG. 2B
(PRIOR ART)

$$\text{CORRELATION WINDOW} = N + N_{guard} - N_{sp}$$

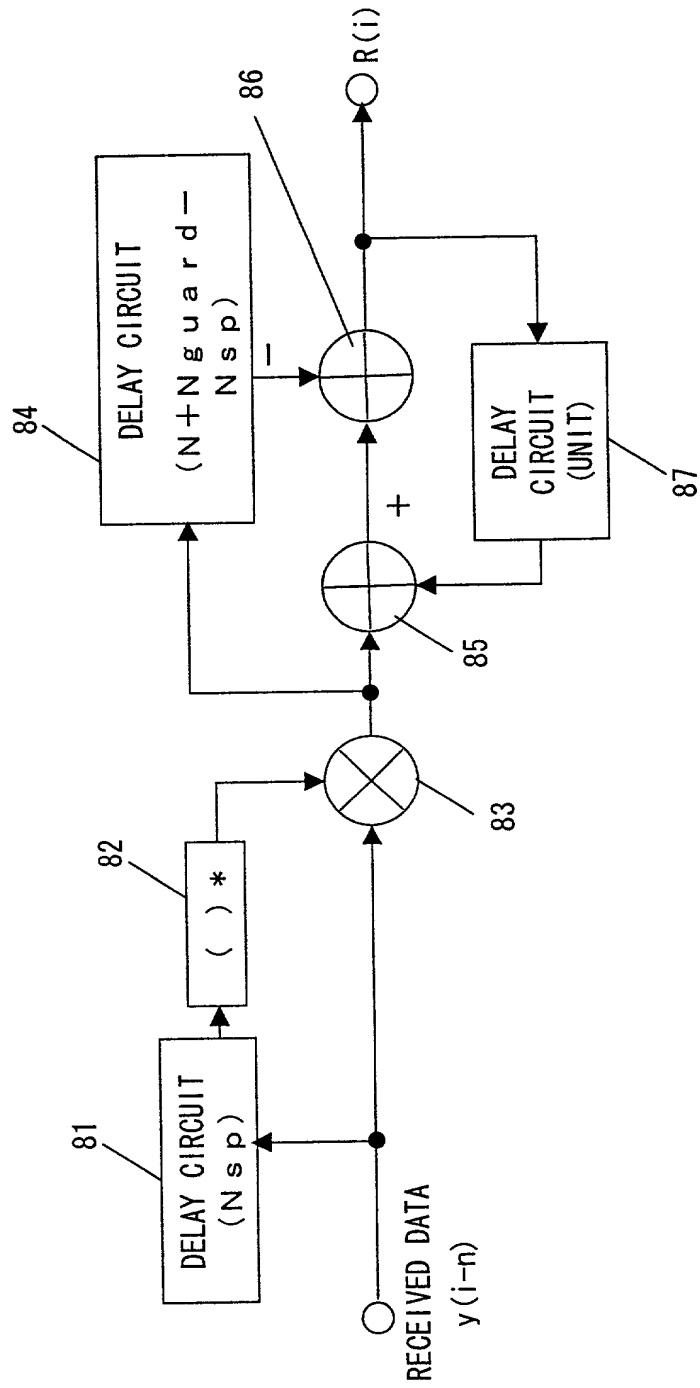


FIG. 3 (PRIOR ART)

IEEE802.11a : B16x10+C32+C64x2

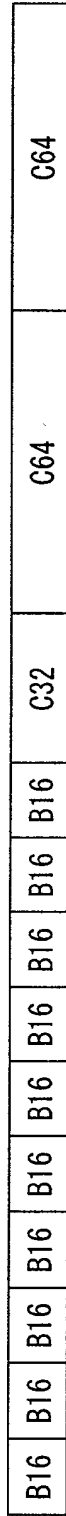


FIG. 4 (PRIOR ART)

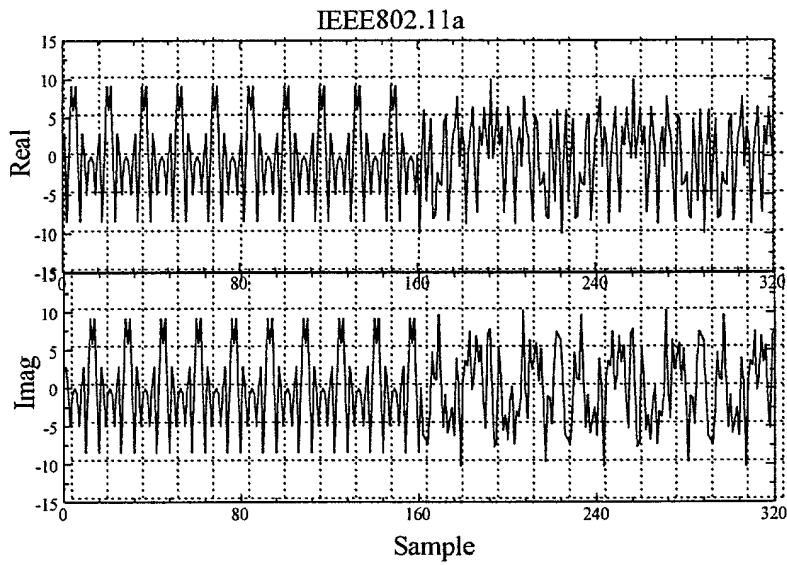


FIG. 5 (PRIOR ART)

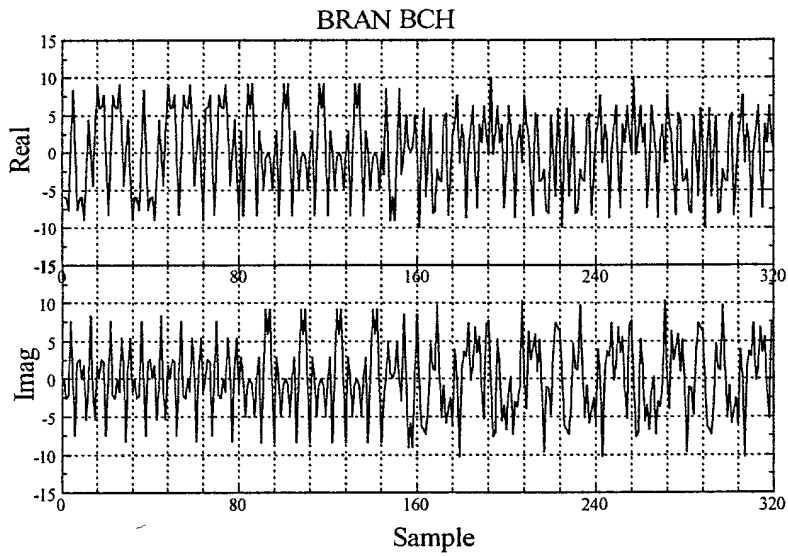


FIG. 7 (PRIOR ART)

BRAN BCH : A16x5+B16x5+C32+C64x2

| | | | | | | | | | | | | | |
|-----|------|-----|------|------|-----|-----|-----|-----|-----|------|-----|-----|-----|
| A16 | IA16 | A16 | IA16 | IA16 | B16 | B16 | B16 | B16 | B16 | IB16 | C32 | C64 | C64 |
|-----|------|-----|------|------|-----|-----|-----|-----|-----|------|-----|-----|-----|

FIG. 6 (PRIOR ART)

BRAN UL : B16x10+C32+C64x2

| | | | | | | | | | | | | | | | | |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|------|-----|-----|-----|
| B16 | B16 | B16 | B16 | B16 | B16 | B16 | B16 | B16 | B16 | B16 | B16 | B16 | IB16 | C32 | C64 | C64 |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|------|-----|-----|-----|

FIG. 8 (PRIOR ART)

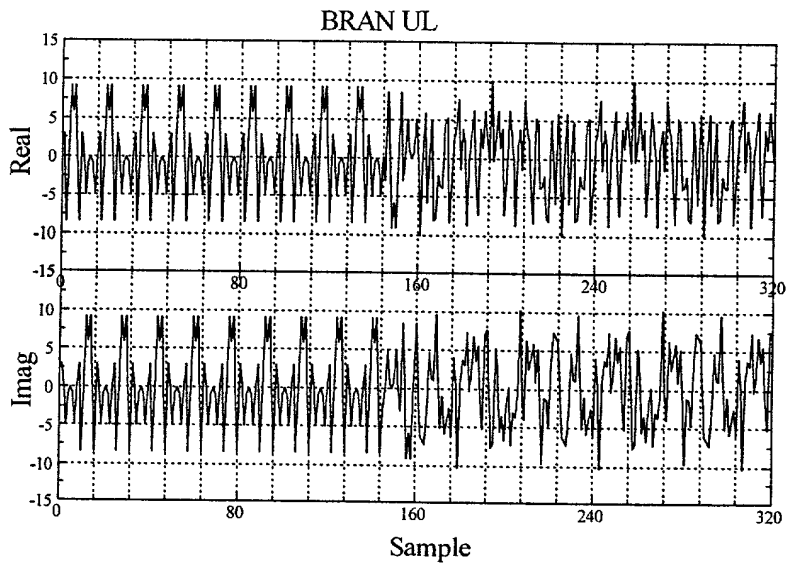


FIG. 9 (PRIOR ART)

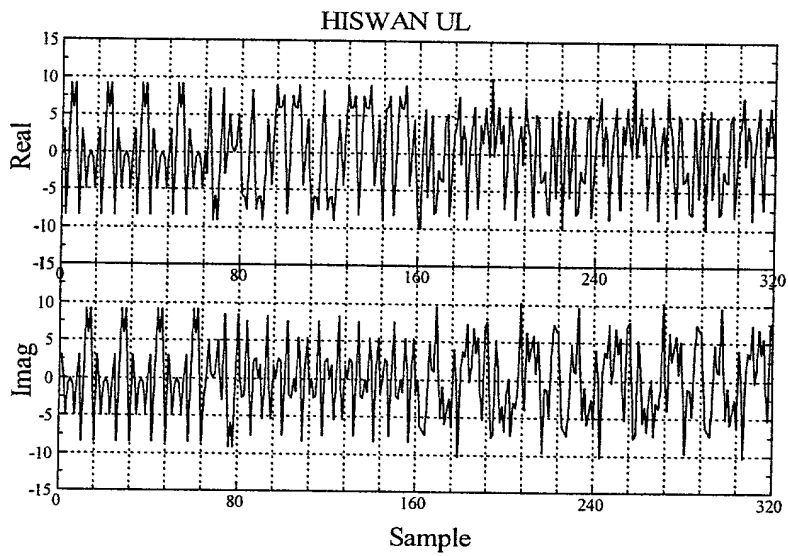


FIG. 11 (PRIOR ART)

HISWAN UL : B16x5+A16x5+C32+C64x2

| | | | | | | | | | | | | | | | |
|-----|-----|-----|-----|-----|------|-----|-----|-----|-----|-----|------|------|-----|-----|-----|
| B16 | B16 | B16 | B16 | B16 | IB16 | A16 | A16 | A16 | A16 | A16 | IA16 | IA16 | C32 | C64 | C64 |
|-----|-----|-----|-----|-----|------|-----|-----|-----|-----|-----|------|------|-----|-----|-----|

FIG. 10 (PRIOR ART)

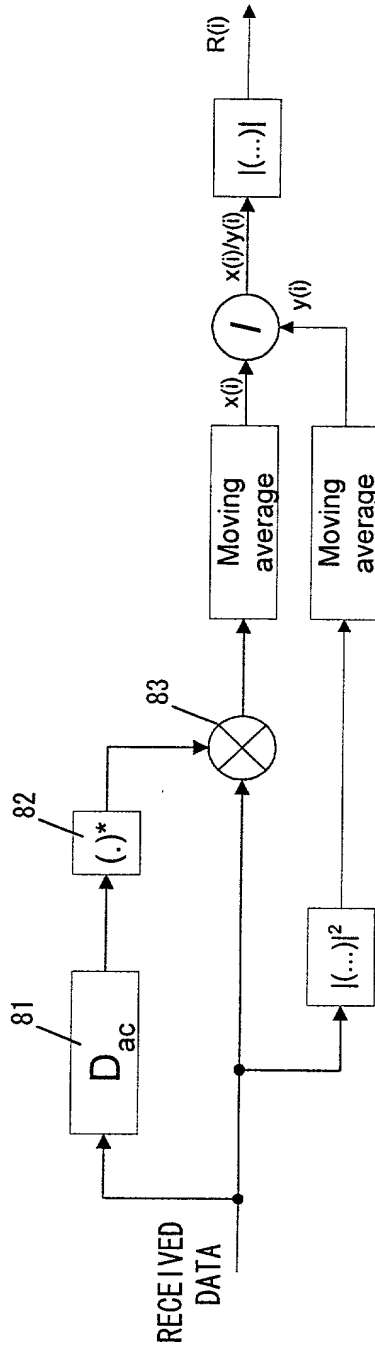


FIG. 12 (PRIOR ART)

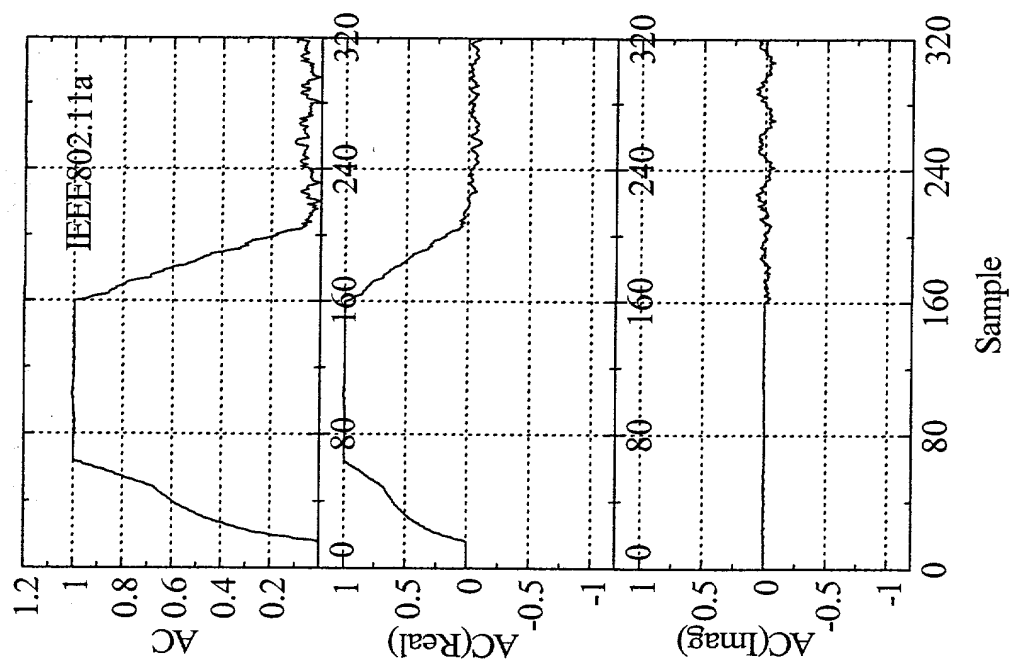


FIG. 13A (PRIOR ART)

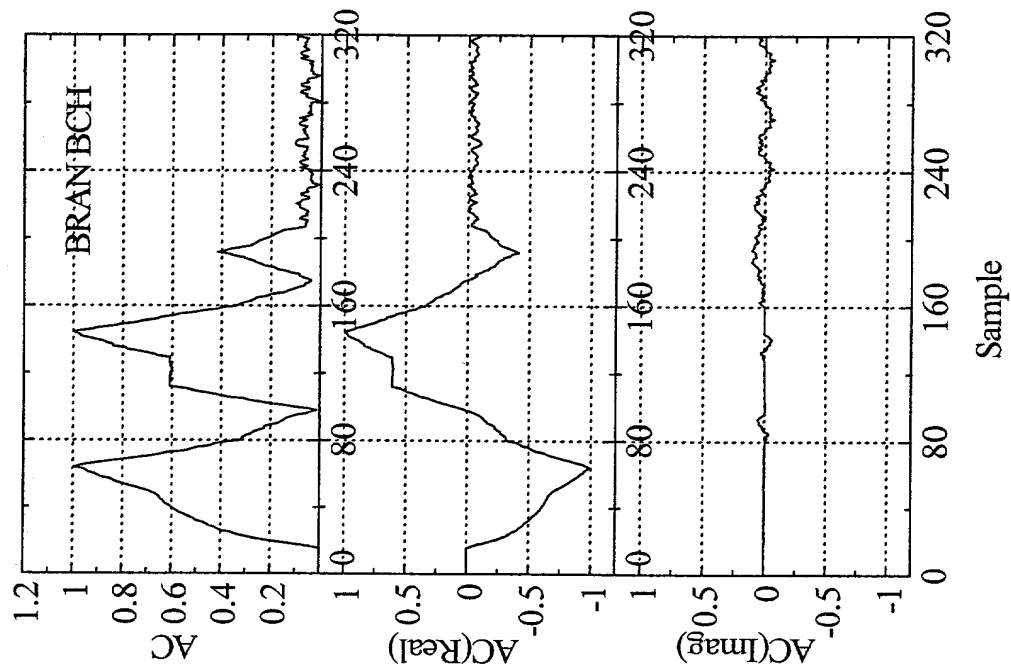


FIG. 13B (PRIOR ART)

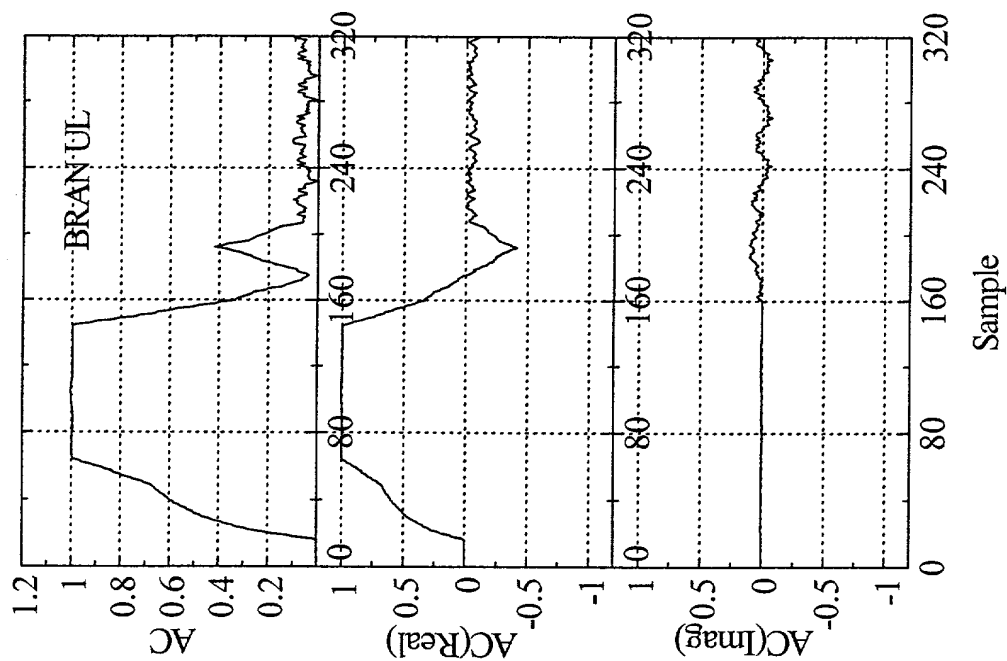


FIG. 14A (PRIOR ART)

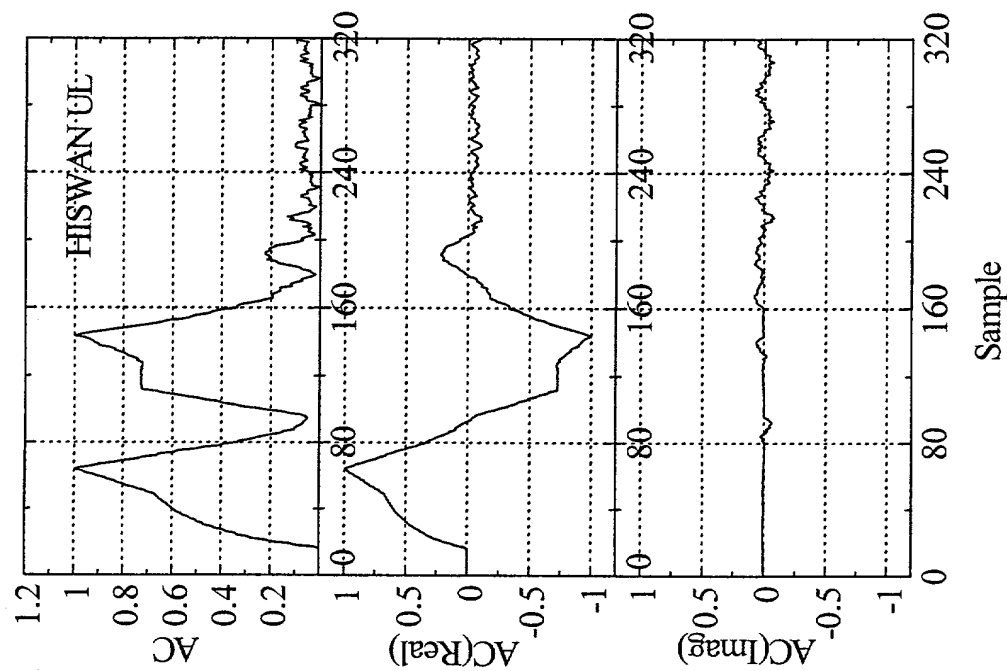


FIG. 14B (PRIOR ART)

| REFERENCE SYMBOL | IEEE802. 11a | BRAN BCH | BRAN UL | HISWAN UL |
|-------------------|--------------|----------|-------------|-----------|
| NUMBER OF PEAKS | 1 (PLAIN) | 2 | 1 (PLAIN) | 2 |
| CODE OF REAL PART | + | -->+ | + | +-->-- |

FIG. 15 (PRIOR ART)

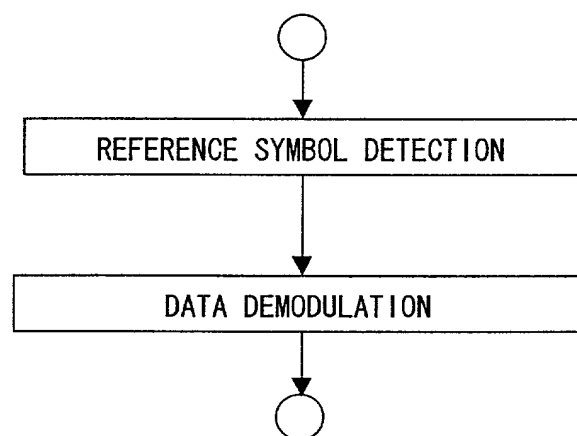


FIG. 16 (PRIOR ART)

WHL (1) : A16x9+C16+C64x2

| | | | | | | | | | | | |
|------|-----|------|-----|-----|------|-----|------|------|------|-----|-----|
| IA16 | A16 | IA16 | A16 | A16 | IA16 | A16 | IA16 | IA16 | IA16 | C16 | C64 |
|------|-----|------|-----|-----|------|-----|------|------|------|-----|-----|

FIG. 17

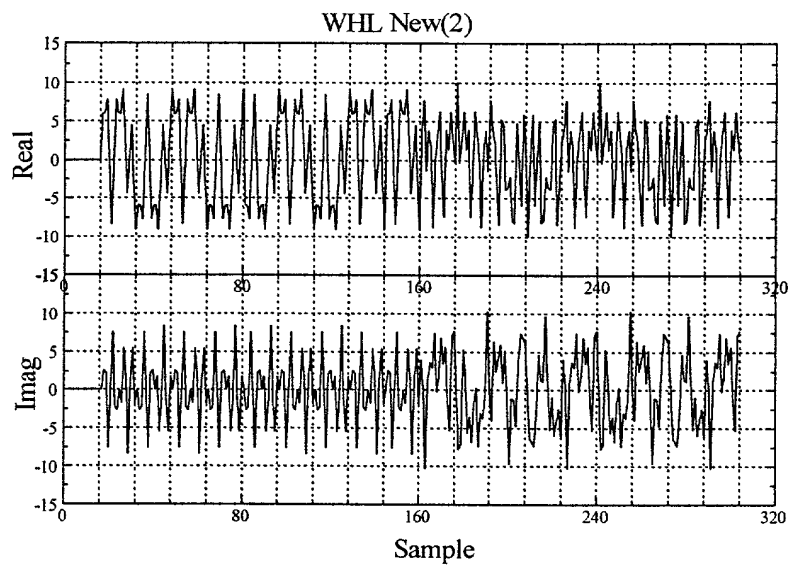


FIG. 18

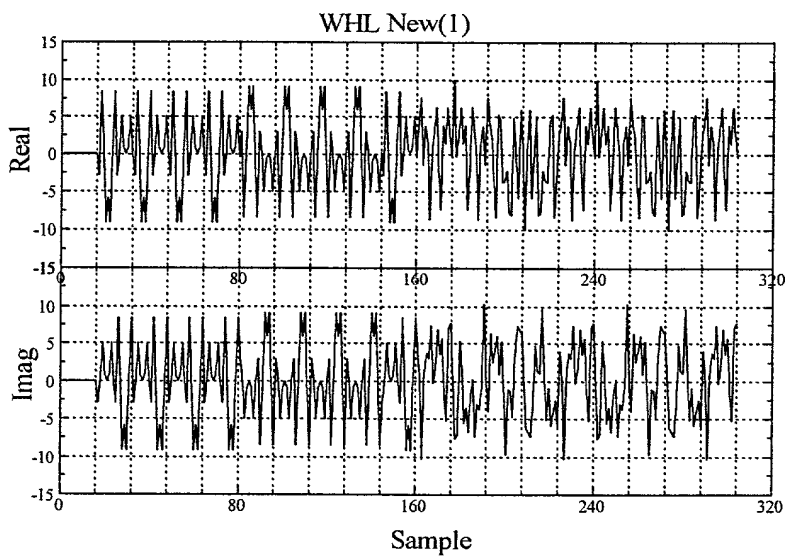


FIG. 21

FILED "44240850

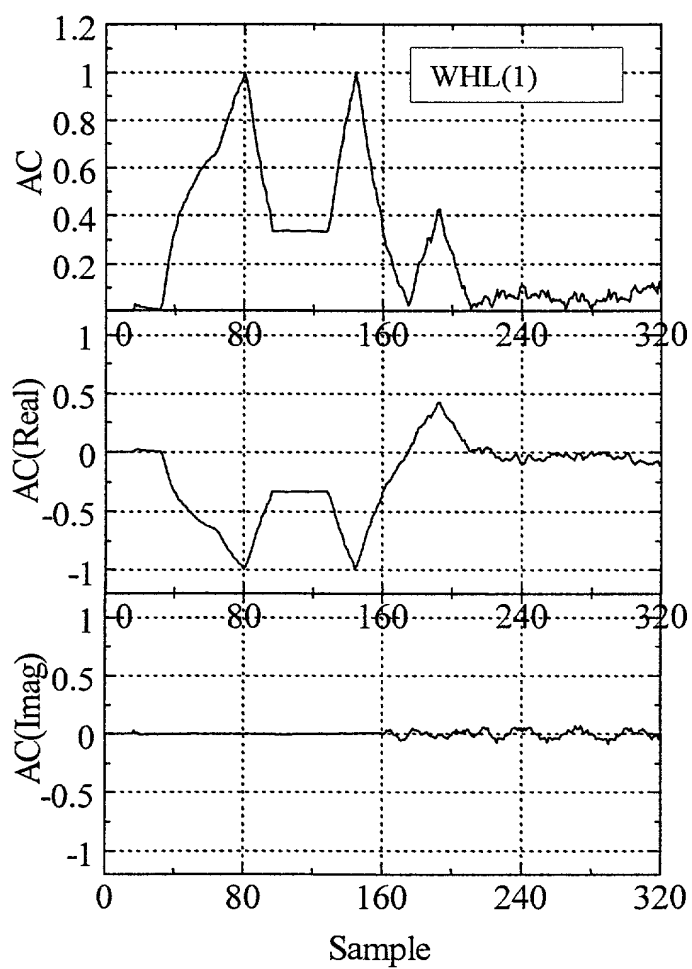


FIG. 19

WHL (2) : B16x9+C16+C64x2

| | | | | | | | | | | | | | | |
|------|------|------|------|------|------|------|------|------|------|------|------|-----|-----|-----|
| IB16 | IB16 | IB16 | IB16 | IB16 | IB16 | IB16 | IB16 | IB16 | IB16 | IB16 | IB16 | C16 | C64 | C64 |
|------|------|------|------|------|------|------|------|------|------|------|------|-----|-----|-----|

FIG. 20

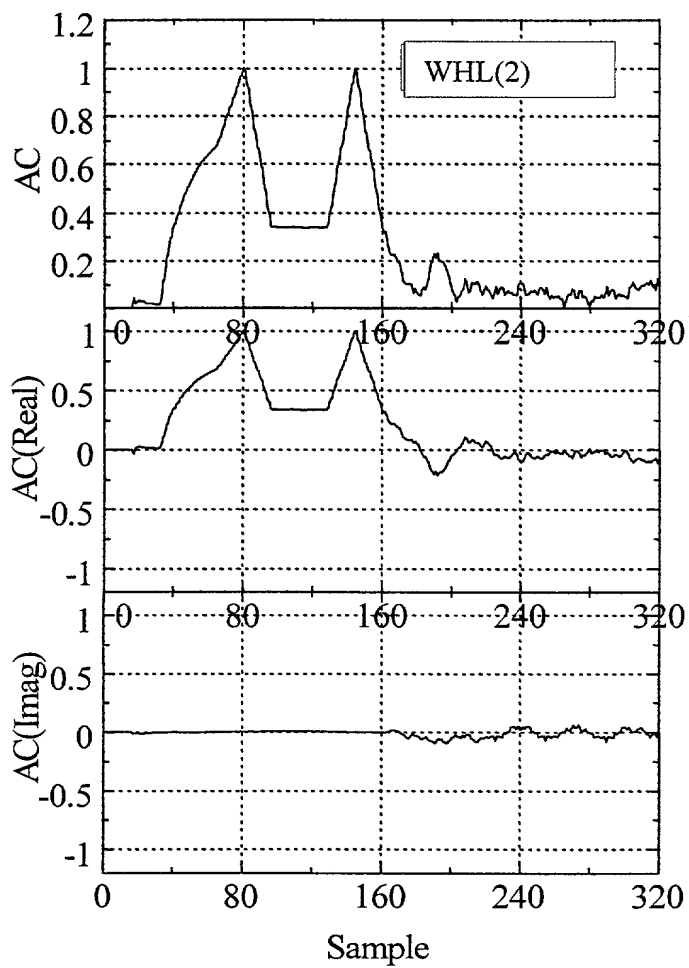


FIG. 22

| REFERENCE SYMBOL | SERIES (1) | SERIES (2) |
|-------------------|--------------|--------------|
| NUMBER OF PEAKS | 2 | 2 |
| CODE OF REAL PART | -->-- | +>+ |

FIG. 23

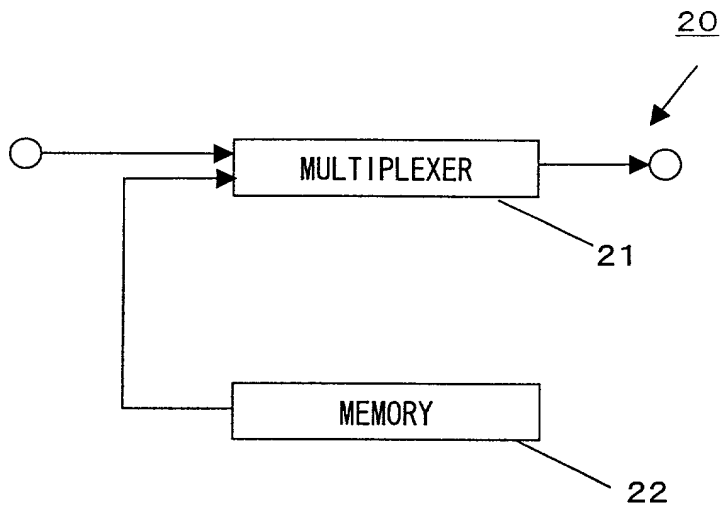


FIG. 26

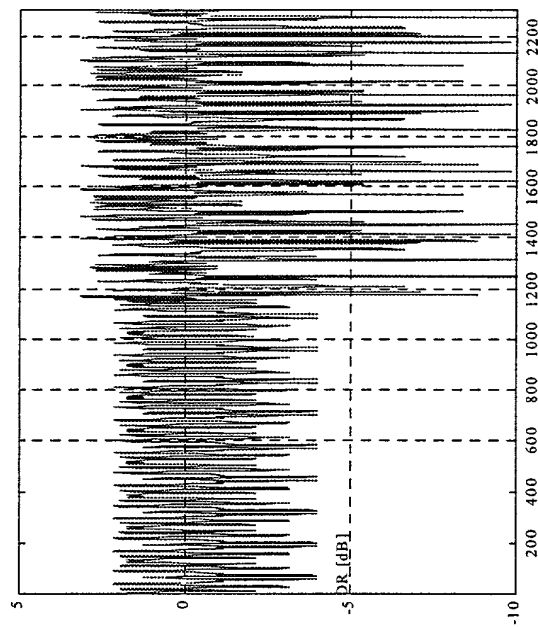


FIG. 24A

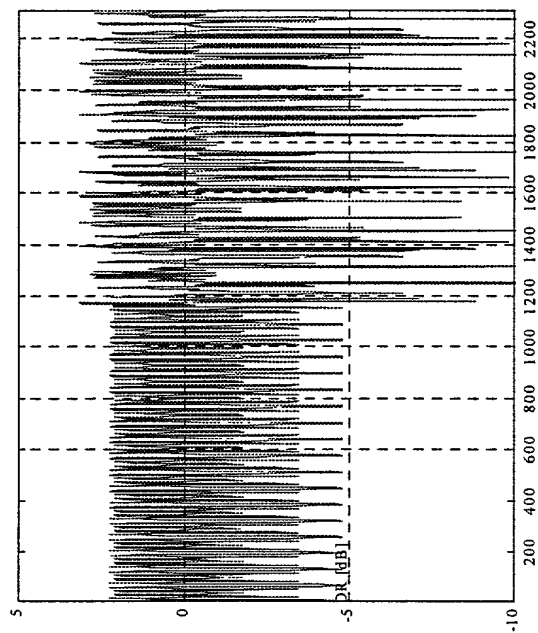


FIG. 24B

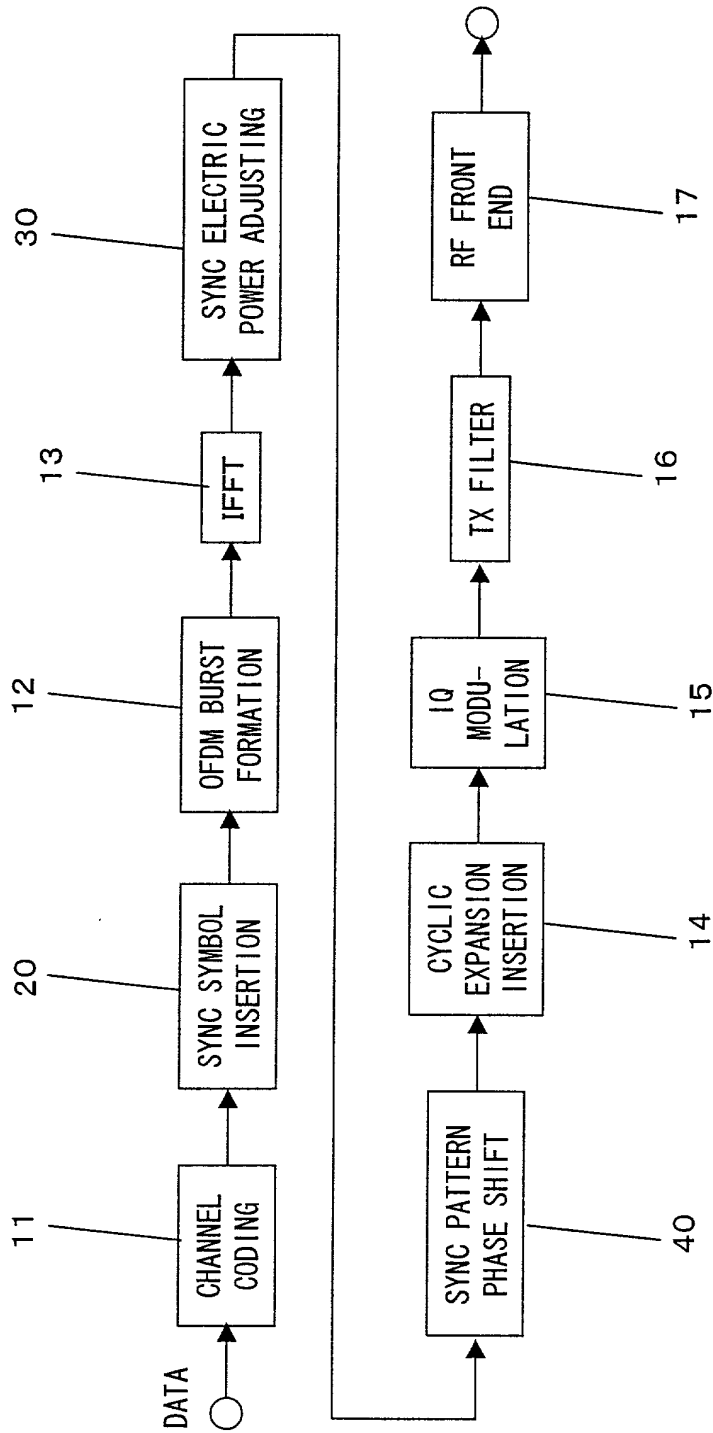


FIG.25

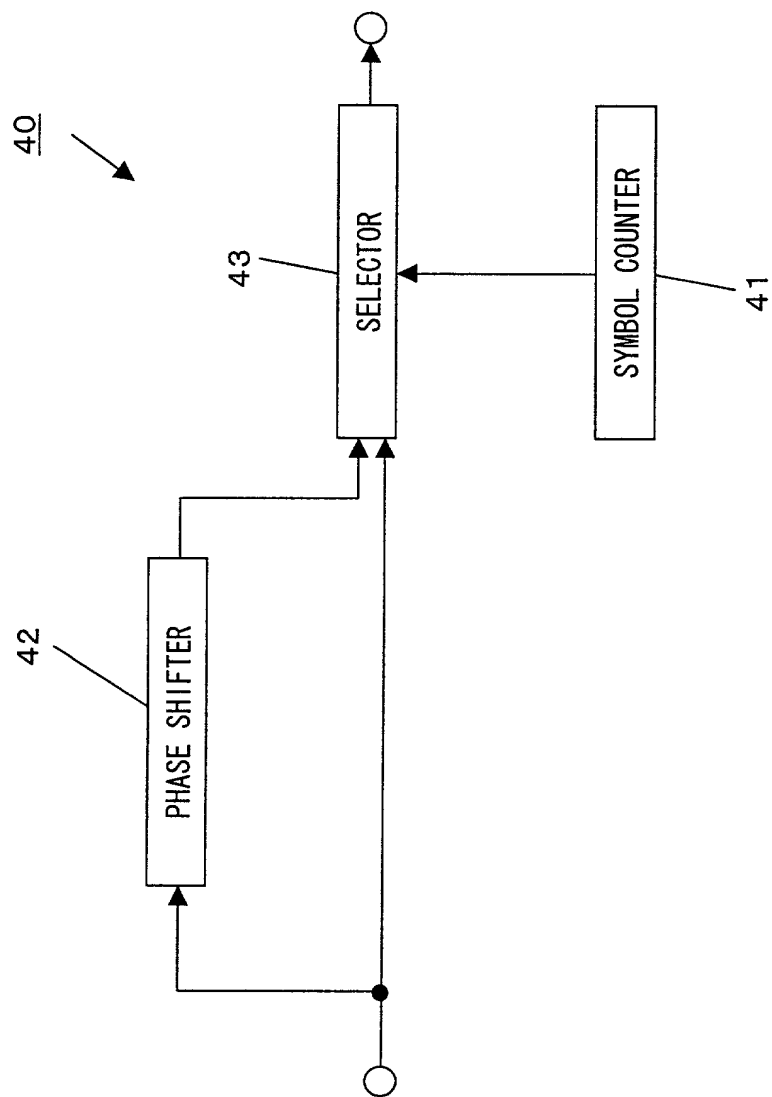


FIG. 27

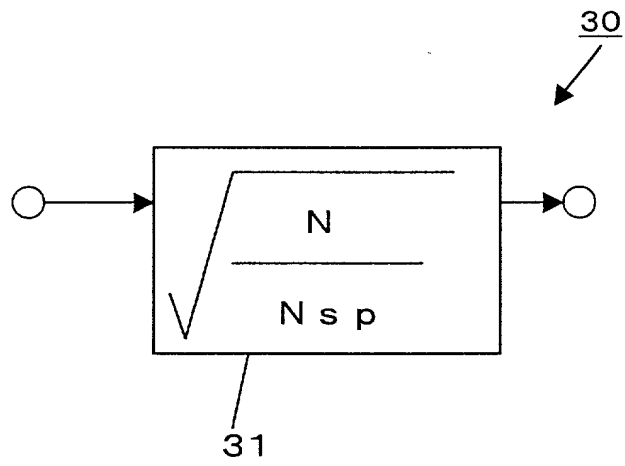


FIG. 28

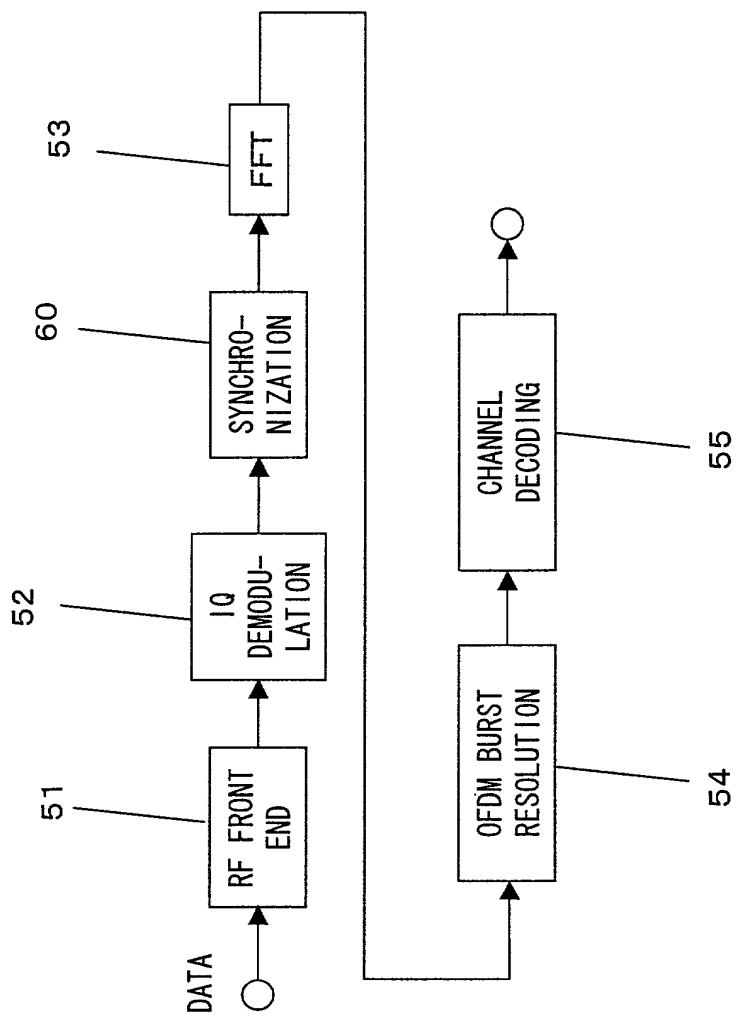


FIG. 29

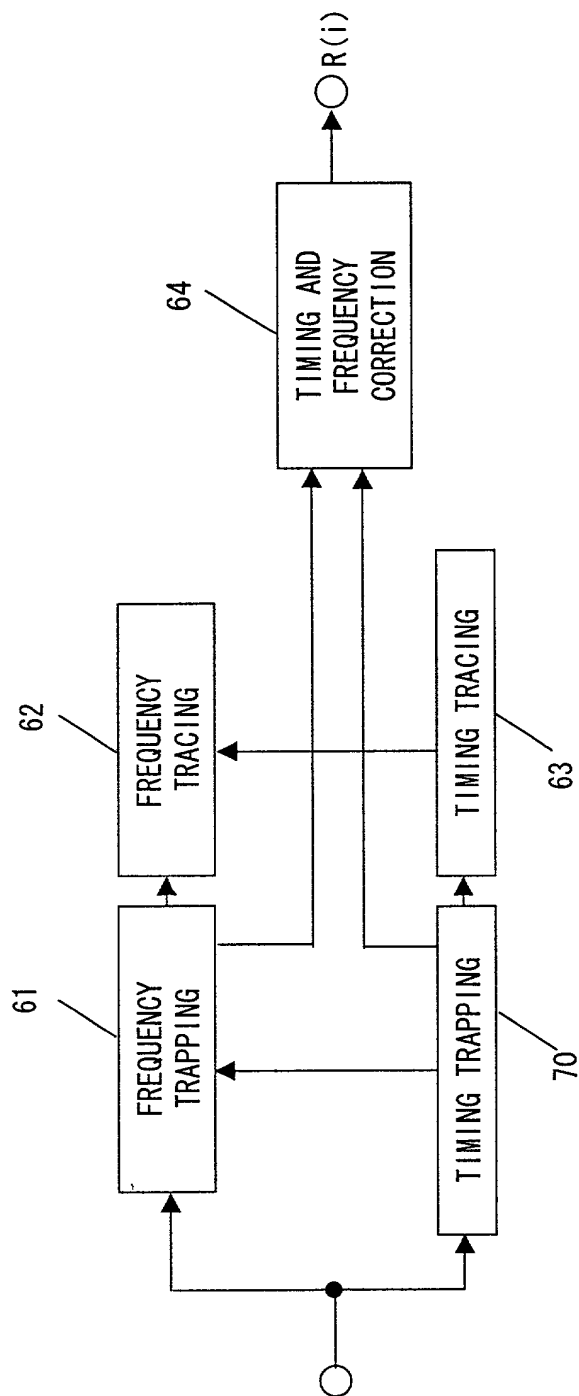


FIG. 30

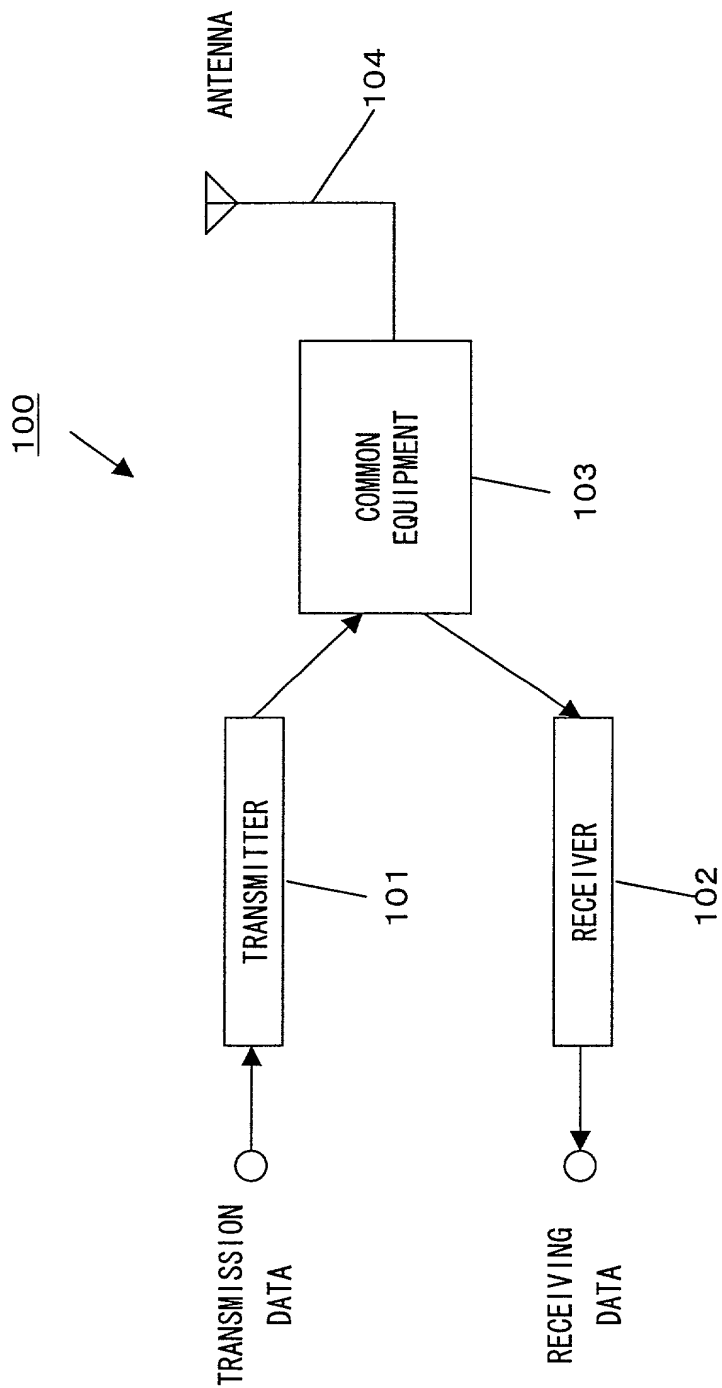


FIG. 32

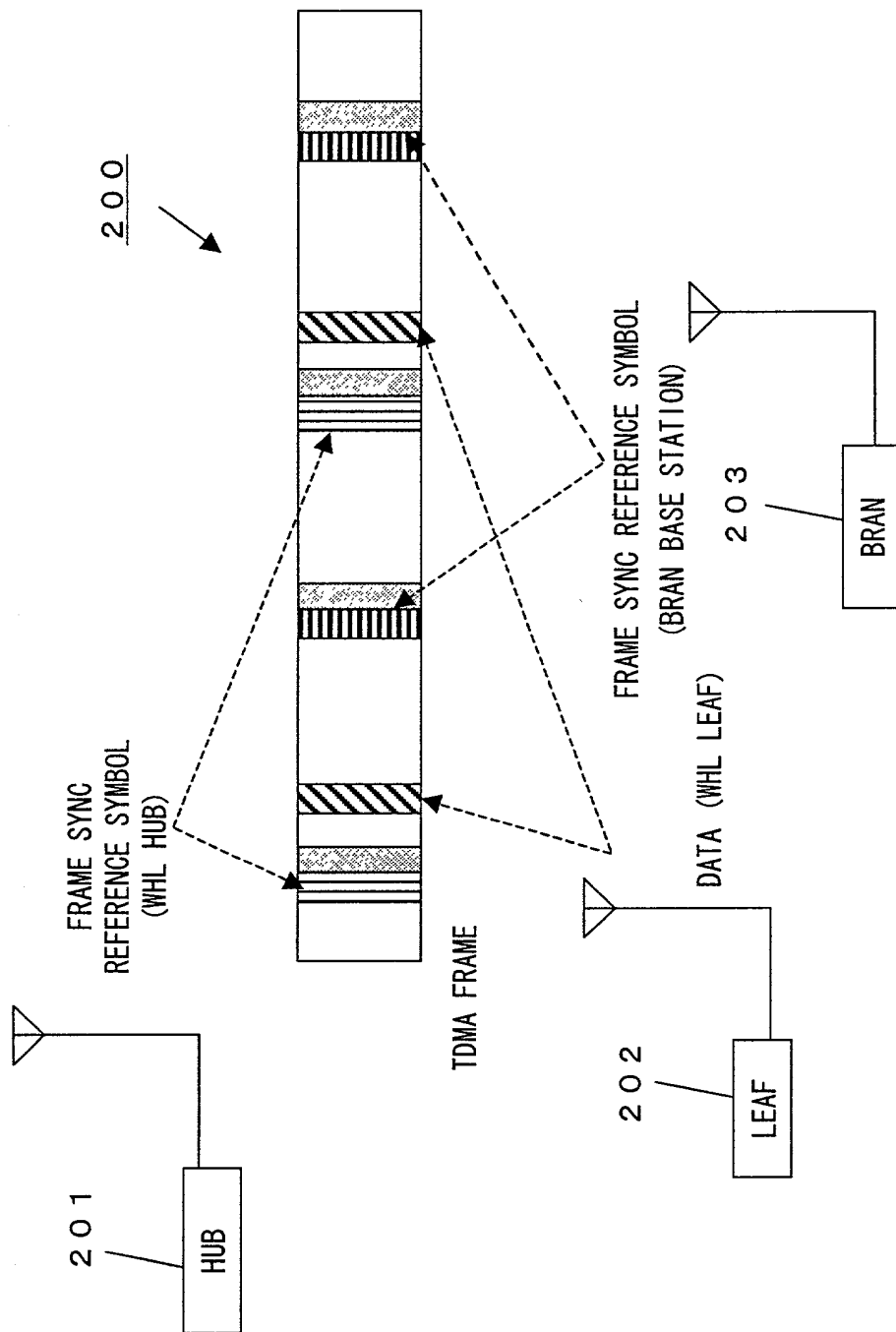


FIG. 33

WHL (1) : A16x9+X16+C16+C64x2

| | | | | | | | | | | | | | |
|------|-----|------|------|-----|-----|------|-----|------|------|---|-----|-----|-----|
| IA16 | A16 | IA16 | IA16 | A16 | A16 | IA16 | A16 | IA16 | IA16 | X | C16 | C64 | C64 |
|------|-----|------|------|-----|-----|------|-----|------|------|---|-----|-----|-----|

FIG. 34A

WHL (1) : X16+A16x9+C16+C64x2

| | | | | | | | | | | | | | |
|---|------|-----|------|------|-----|-----|------|-----|------|------|-----|-----|-----|
| X | IA16 | A16 | IA16 | IA16 | A16 | A16 | IA16 | A16 | IA16 | IA16 | C16 | C64 | C64 |
|---|------|-----|------|------|-----|-----|------|-----|------|------|-----|-----|-----|

FIG. 34B

WHL (2) : B16x9+X16+C16+C64x2

| | | | | | | | | | | | | | | |
|------|------|------|------|------|-----|-----|-----|-----|-----|------|---|-----|-----|-----|
| IB16 | IB16 | IB16 | IB16 | IB16 | B16 | B16 | B16 | B16 | B16 | IB16 | X | C16 | C64 | C64 |
|------|------|------|------|------|-----|-----|-----|-----|-----|------|---|-----|-----|-----|

FIG. 35A

WHL (2) : X16+B16x9+C16+C64x2

| | | | | | | | | | | | | | | |
|---|------|------|------|------|------|-----|-----|-----|-----|-----|------|-----|-----|-----|
| X | IB16 | IB16 | IB16 | IB16 | IB16 | B16 | B16 | B16 | B16 | B16 | IB16 | C16 | C64 | C64 |
|---|------|------|------|------|------|-----|-----|-----|-----|-----|------|-----|-----|-----|

FIG. 35B